

# IIT secures patent for data-sensing technique

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IIT Indore received a patent on 'Offset-Compensated Data Sensing Technique for Low Energy Embedded SRAM' from the Patent Office, Government of India. The inventors of the patent are Dr Bhupendra Singh Reniwal and professor Santosh Kumar Vishvakarma.

This invention has led to the development of a semiconductor memory architecture designed for low-energy electronic devices which can tolerate variations in the modern IC fabrication process.

The conventional micro-processor-based, hand-held devices comprises of embedded memory which characterises a huge share of the system-on-chip (SoC).

There is a likelihood of functional failure because of offset during chip fabri-



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cation. This technique will enable the emerging chips to be fast enough to read the data from processor on-chip memory. It will also increase the accuracy and speed and save energy for data processing in Neuro-morphic computing chips, low-energy wearable electronics devices, smartphones, personal healthcare assistants, and smart Cyber-Physical Systems applications.

This is the 16th patent of the institute among 85 patents filed till date. //